Serial No. 10/743,653 Confirm. No.: 7580 Art Unit: 2618 Examiner: Nguyen, Duc M.

IBM Docket: AUS920030892US1(4021)

## **AMENDMENT OF THE CLAIMS**

## 1. -17. (Cancelled)

- 18[[7]]. (Currently Amended) A method for reducing power consumption by [[a]]multiple links between an origin and a destination of a data transmission, the method comprising:
  - determining an activity for the <u>multiple\_links</u> based upon forwarding logic, the activity being related to a characteristic for [[a]]the data transmission via a channel of the <u>multiple\_links</u>;
  - associating the activity with a power mode for the <u>multiple</u> links, wherein the power mode is related to the characteristic; and
  - communicating the power mode to the multiple links to configure[[ing]] circuitry associated with the multiple links to operate in the power mode to process the data transmission.
- 19[[8]]. (Currently Amended) The method of claim 18[[7]], wherein determining comprises selecting a medium for at least one of the channels.
- <u>20</u>[[19]]. (Currently Amended) The method of claim 18[[7]], wherein determining comprises determining that the <u>multiple links</u> [[is]] are inactive.
- $2\underline{1}[[0]]$ . (Currently Amended) The method of claim  $1\underline{8}[[7]]$ , wherein associating comprises associating the transmission frequency with a configuration of the circuitry.
- 22[[1]]. (Currently Amended) The method of claim 18[[7]], wherein <u>communicating</u> comprises <u>communicating</u> the power mode to substitute[[ing]] a clock and data recovery loop with a less complex, clock and data recovery loop associated with a lower power consumption.
- 23[[2]]. (Currently Amended) The method of claim 18[[7]], wherein <u>communicating</u>eonfiguring comprises <u>communicating</u> the <u>power mode to reduce[[ing]]</u> a gain of a bias circuit.

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24[[3]]. (Currently Amended) The method of claim 18[[7]], wherein <u>communicating</u> eonfiguring comprises <u>communicating</u> the power mode to reduce[[ing]] a frequency of a serialization circuit.

- 25. (New) An apparatus to process a data transmission between a first device and a second device via an intermediate link, the apparatus comprising:
  - a first port comprising a first link circuit to couple to the first device to process the data transmission for the first device and a first local link control responsive to a control signal received by the apparatus from a global link control to configure the first link circuit to operate in a first power mode of multiple power modes associated with the first link circuit based upon the control signal, wherein the control signal is associated with at least one characteristic of the data transmission and the first power mode is associated with the at least one characteristic; and
  - a second port comprising a second link circuit to couple to the second device to process the data transmission for the second device and a second local link control responsive to the control signal from the global link control to configure the second link circuit to operate in a second power mode of multiple power modes associated with the second link circuit, wherein the second power mode is associated with the at least one characteristic.
- 26. (New) The apparatus of claim 25, wherein the first link circuit comprises a clock and data recovery loop, wherein an ability of the clock and data recovery loop to track changes in a phase of the data transmission varies between the first power mode and other of the multiple power modes associated with the first link circuit.
- 27. (New) The apparatus of claim 25, wherein the first link circuit comprises circuitry that is configurable to adjust amplification of the data transmission.
- 28. (New) The apparatus of claim 25, wherein the first link circuit comprises a gain and equalization circuit being configurable via the first local link control in response to the

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control signal to compensate for attenuation and distortion based upon a medium

associated with the data transmission.

29. (New) The apparatus of claim 31, wherein first local link control comprises

interpretation logic to reconfigure the first link circuit in response to the control signal,

wherein the control signal is related to a traffic type associated with the data

transmission.

30. (New) The apparatus of claim 25, wherein the first local link control comprises

interpretation logic to adjust an operating voltage and frequency for the first link circuit

in response to an indication by the control signal of a data frequency for the data

transmission.

31. (New) The apparatus of claim 25, wherein the second link circuit comprises a

serialization circuit that is configurable via the second local link control to process the

data transmission in accordance with a data frequency indicated by the control signal.

32. (New) The apparatus of claim 25, wherein the second link circuit comprises a pre-

emphasis circuit that is configurable via the second local link control in response to the

control signal.

33. (New) The apparatus of claim 25, wherein the second local link control is to reconfigure

the second link circuit in response to the control signal, wherein the control signal is

indicative of a length of a medium for the data transmission.

34. (New) The apparatus of claim 25, wherein the second local link control comprises

interpretation logic to select the second link circuit and the second power mode for the

second link circuit based upon the control signal.

35. (New) The apparatus of claim 34, wherein the interpretation logic comprises a table to

associate the second power mode with a data frequency, wherein the control signal

indicates the data frequency.

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36. (New) An apparatus, comprising:

forwarding logic to associate ports with multiple links between an origin and a destination for a data transmission to transmit the data transmission via the

multiple links to the destination; and

a global link control coupled with the forwarding logic to transmit a control signal to the

ports, the control signal being indicative of at least one characteristic of the data

transmission, to configure link circuits of the ports based upon the at least one

characteristic via local link controls of the ports.

37. (New) The apparatus of claim 36, further comprising a local link control, responsive to

the control signal, to configure the circuitry associated with at least one of the multiple

links to operate in a power mode associated with the control signal.

38. (New) The apparatus of claim 37, wherein the local link control comprises part of a

receiver and is designed to adjust power consumption by the at least one of the multiple

links by selecting the power mode, wherein the power mode maintains a data throughput.

39. (New) The apparatus of claim 37, wherein the local link control comprises part of a

transmitter, the transmitter being adapted to deactivate a gain and equalization circuit

based upon the control signal.

40. (New) The apparatus of claim 37, wherein the local link control is adapted to change an

operating frequency and an operating voltage for the circuitry based upon the control

signal.

41. (New) The apparatus of claim 36, wherein the global link control is designed to

communicate a routing decision of a router for the ports of the multiple links in the

control signal to the local link controls of the multiple links, wherein the routing decision

determines a data frequency, a traffic type, and a medium type for the data transmission,

to configure the ports in accordance with the routing decision.